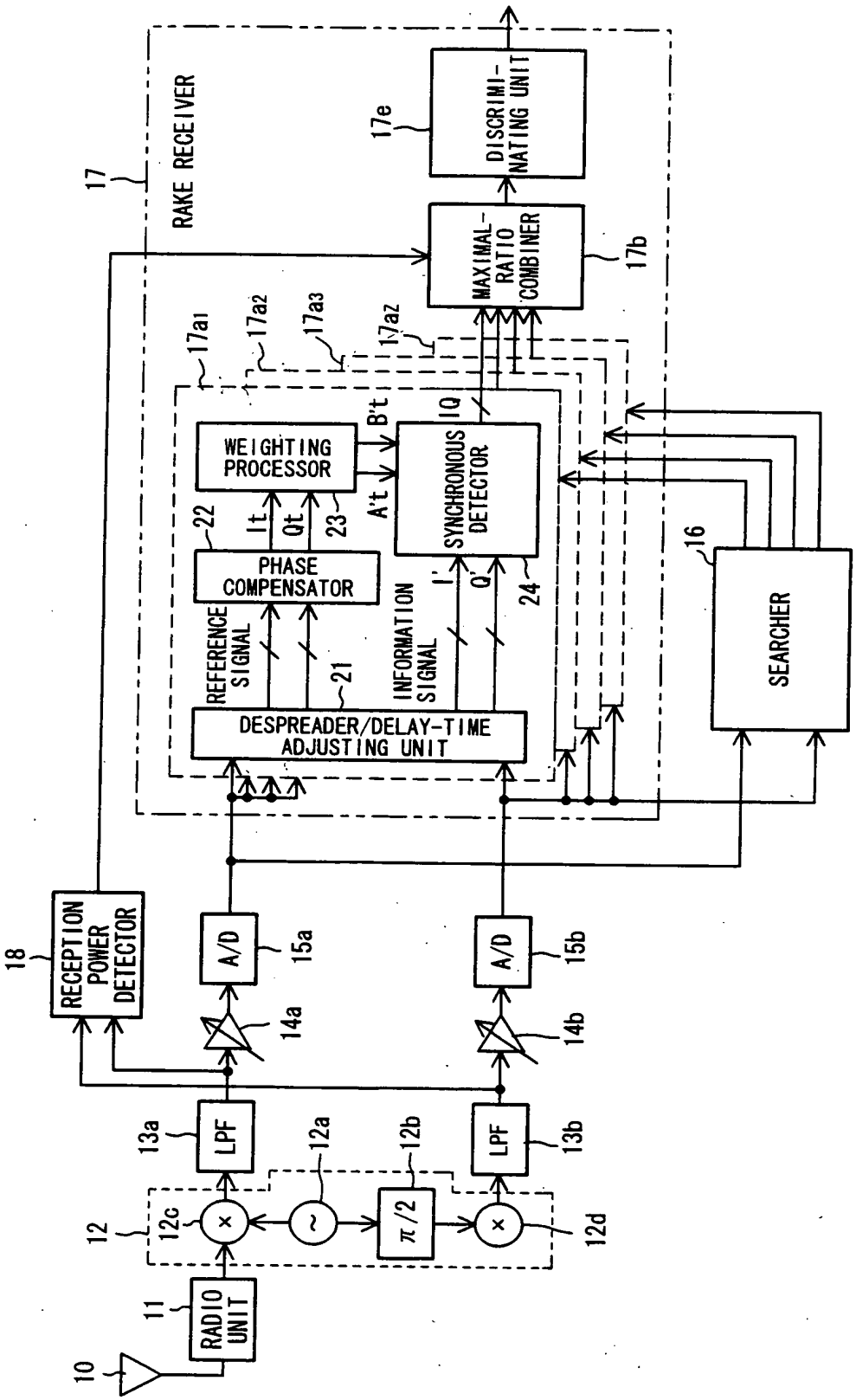
APPROVED BY
O.G. FIG. 1
CLASS 375 SUBCLASS 148
DRAFTSMAN

FIG. 1



35C.F.R. 1.84(a)(1)

APPROVED O.G. FIG.
BY CLASS SUBCLASS

FIG. 2

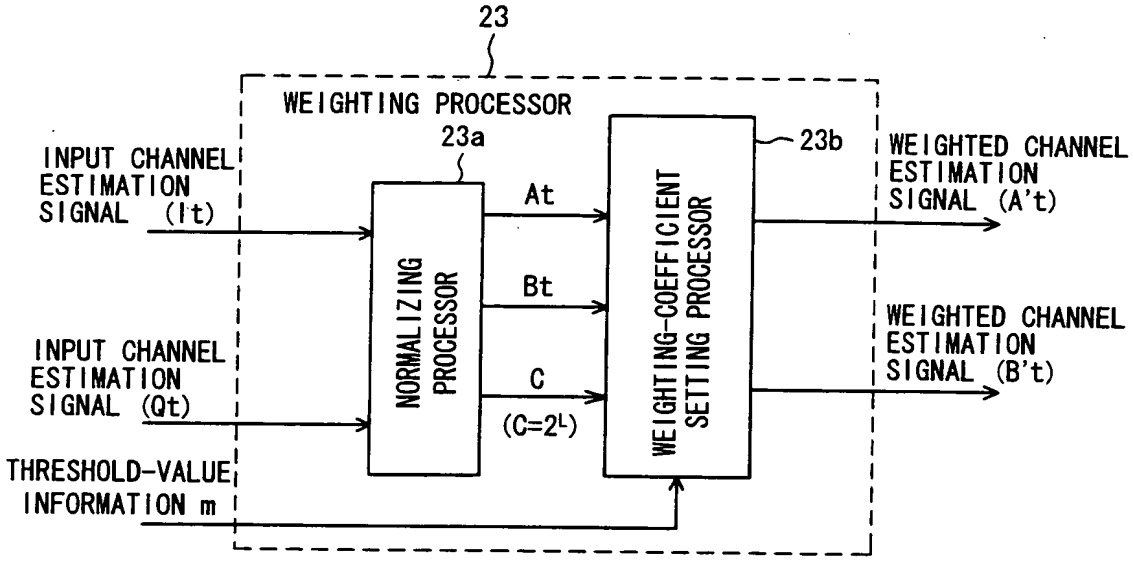
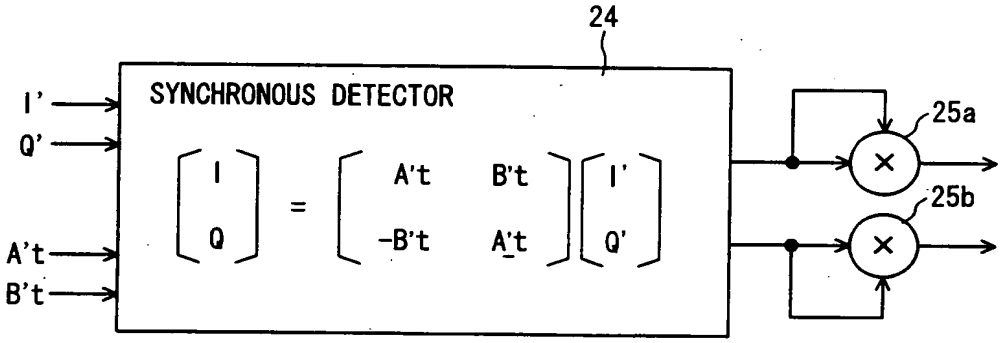
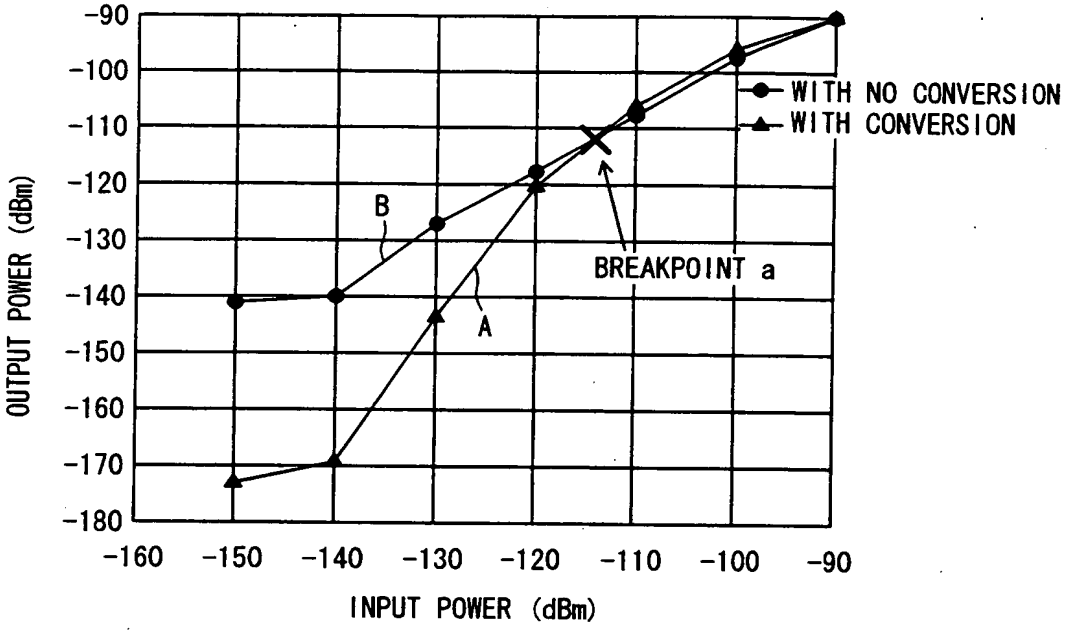


FIG. 3



APPROVED BY DRAFTSMAN | O.G. FIG. CLASS SUBCLASS

FIG. 4



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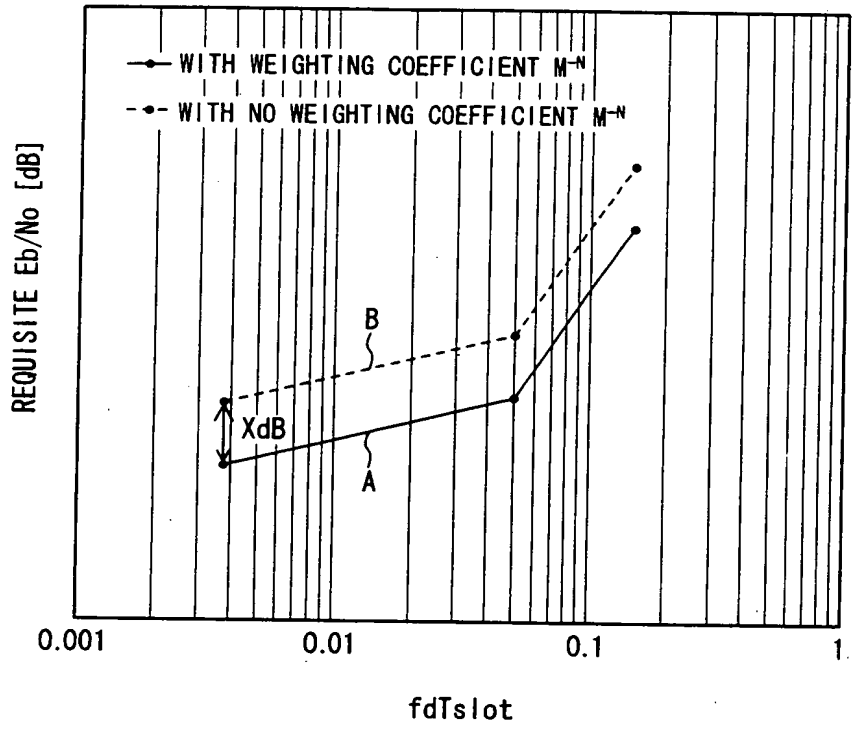
APPROVED O.G. FIG.
BY CLASS SUBCLASS
DRAFTSMAN

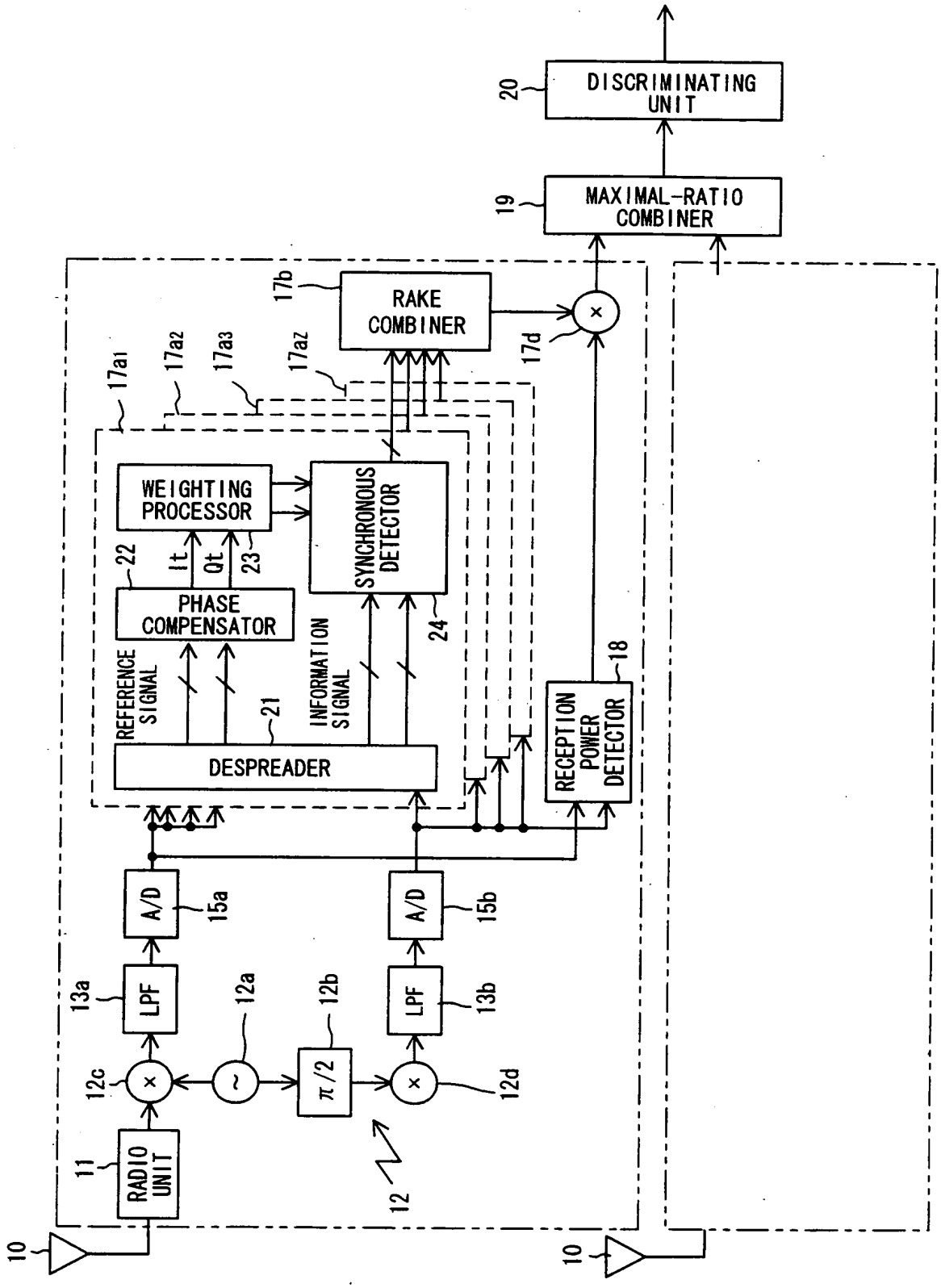
FIG. 5



20040730 052447 007

APPROVED O.G. FIG.
BY CLASS/SUBCLASS
DRAFTSMAN

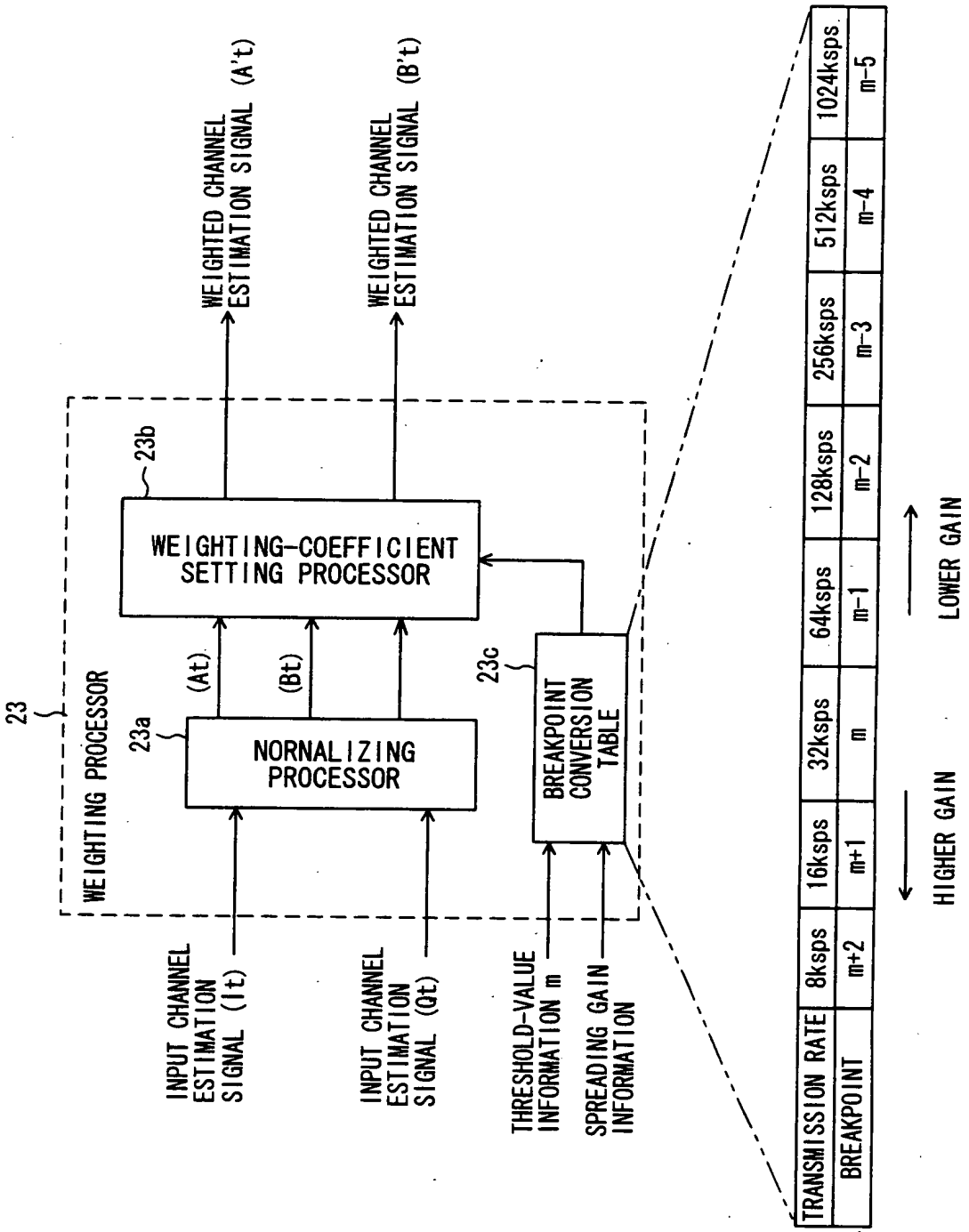
FIG. 6



APPROVED O.G. FIG.
BY CLASS SUBCLASS
DRAFTSMAN

JP2004-064207

FIG. 7



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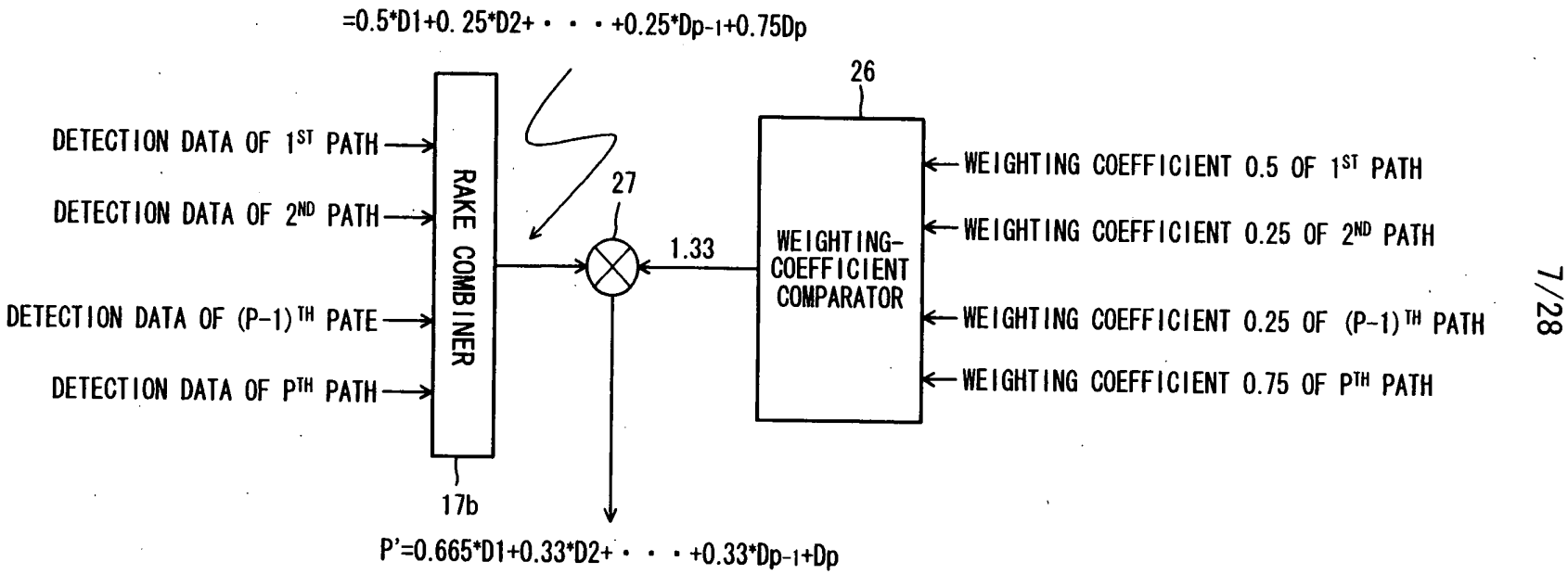
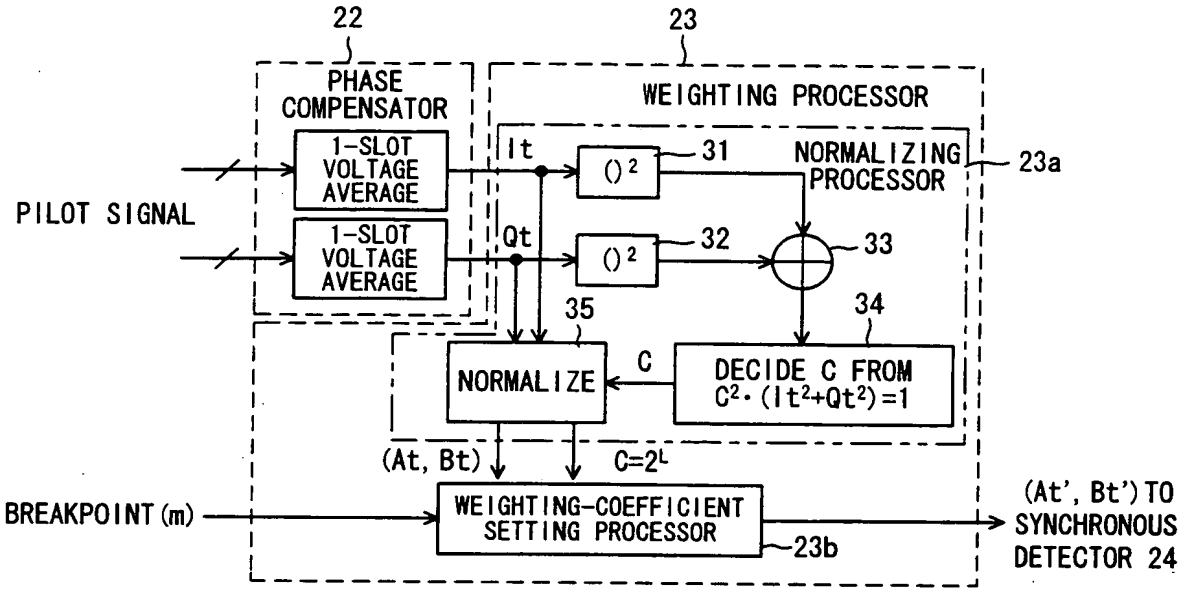
APPROVED O.G. FIG. CLASS/SUBCLASS DRAFTSMAN

FIG. 8



APPROVED O.G. FIG.
BY
DRAFTSMAN

FIG. 9



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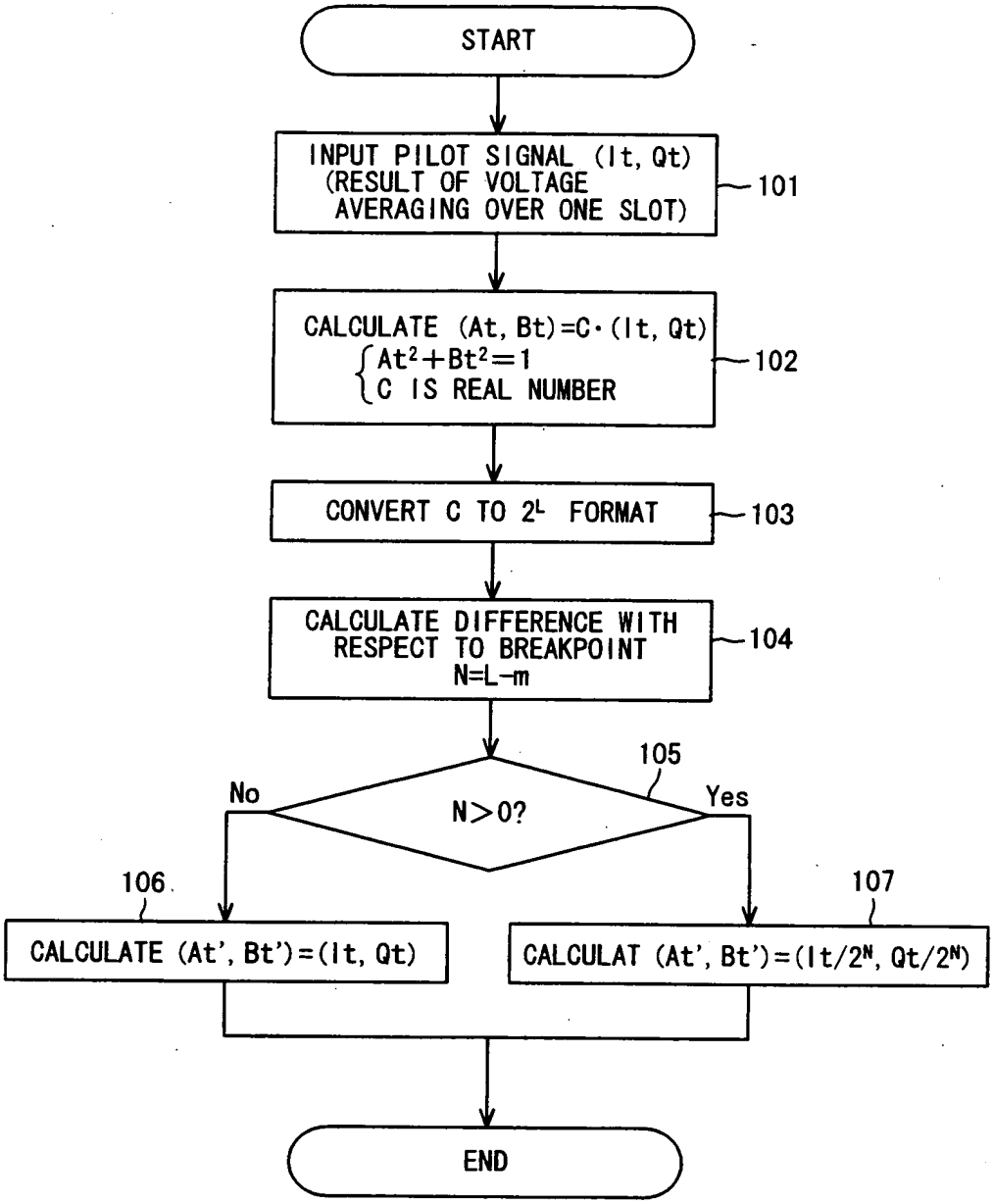
APPROVED
BY
DRAFTSMAN

FIG. 10



APPROVED O.G. FIG.
BY
DRAFTSMAN

FIG. 11

(ex)

WHEN It=0.0271, Qt=0.0156,
M=2, m=4 HOLD

(At, Bt)=32・(0.0271, 0.0156)
=(0.866, 0.5)

C=32 → 2^5 (L=5)

N=1 BECAUSE m=4, L=5 AT N=L−m

N=1>0

(At', Bt')=(0.01355, 0.0078)

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APPROVED O.G. FIG.
CLASS SUBCLASS

FIG. 12

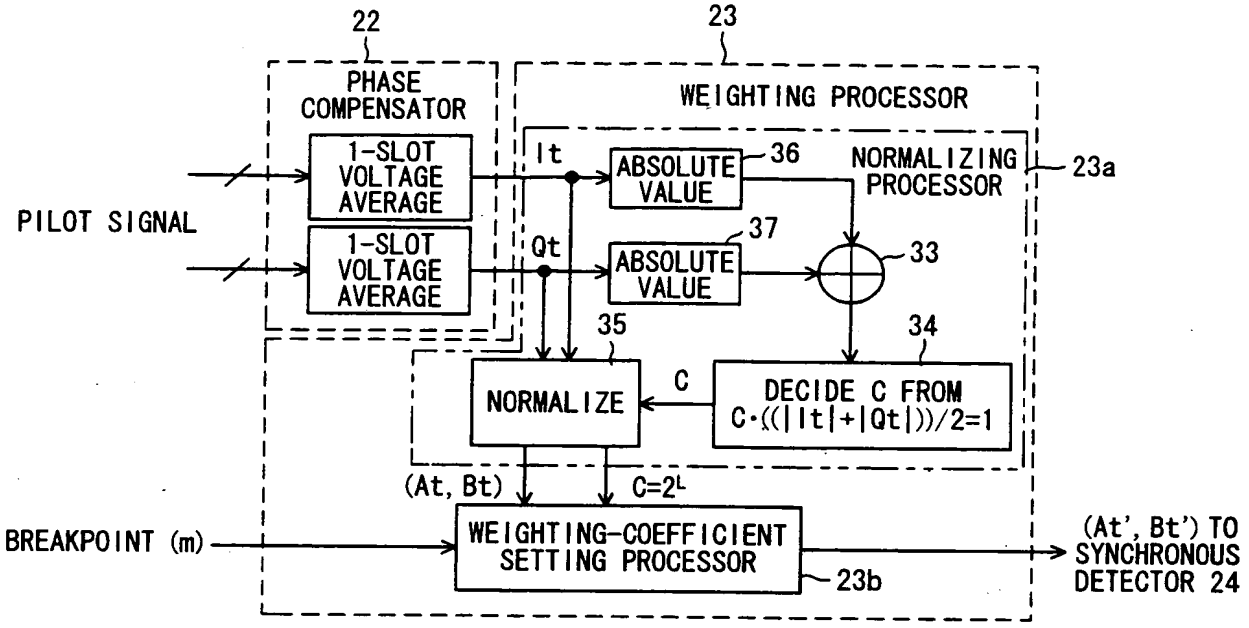
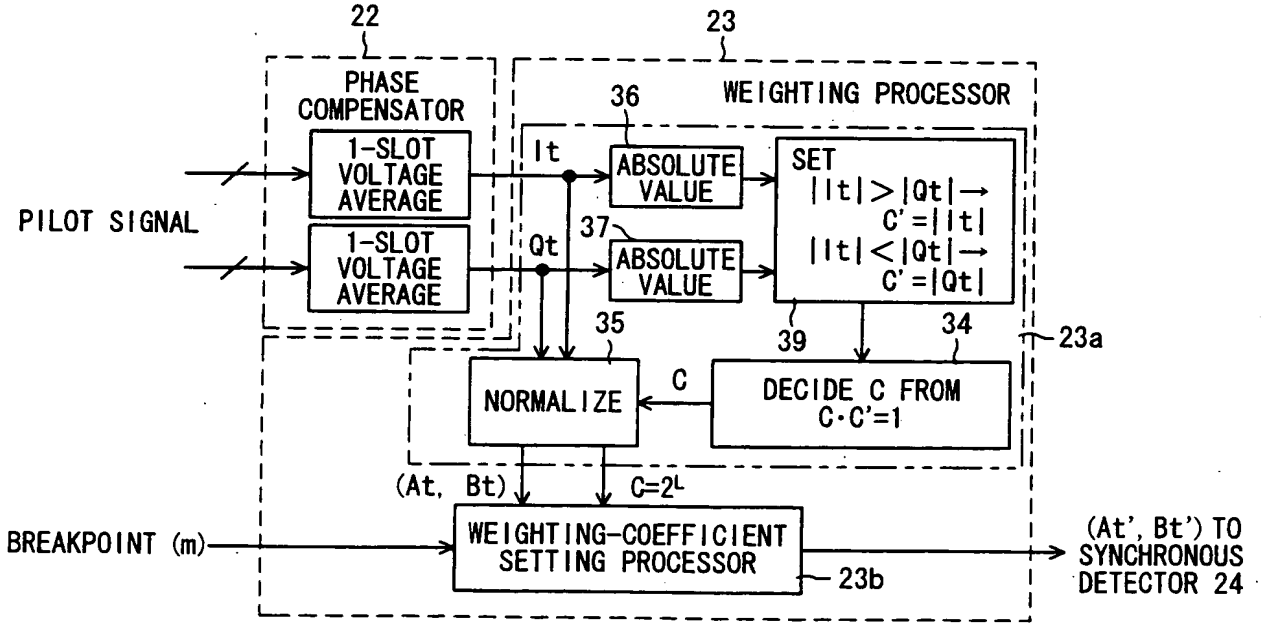
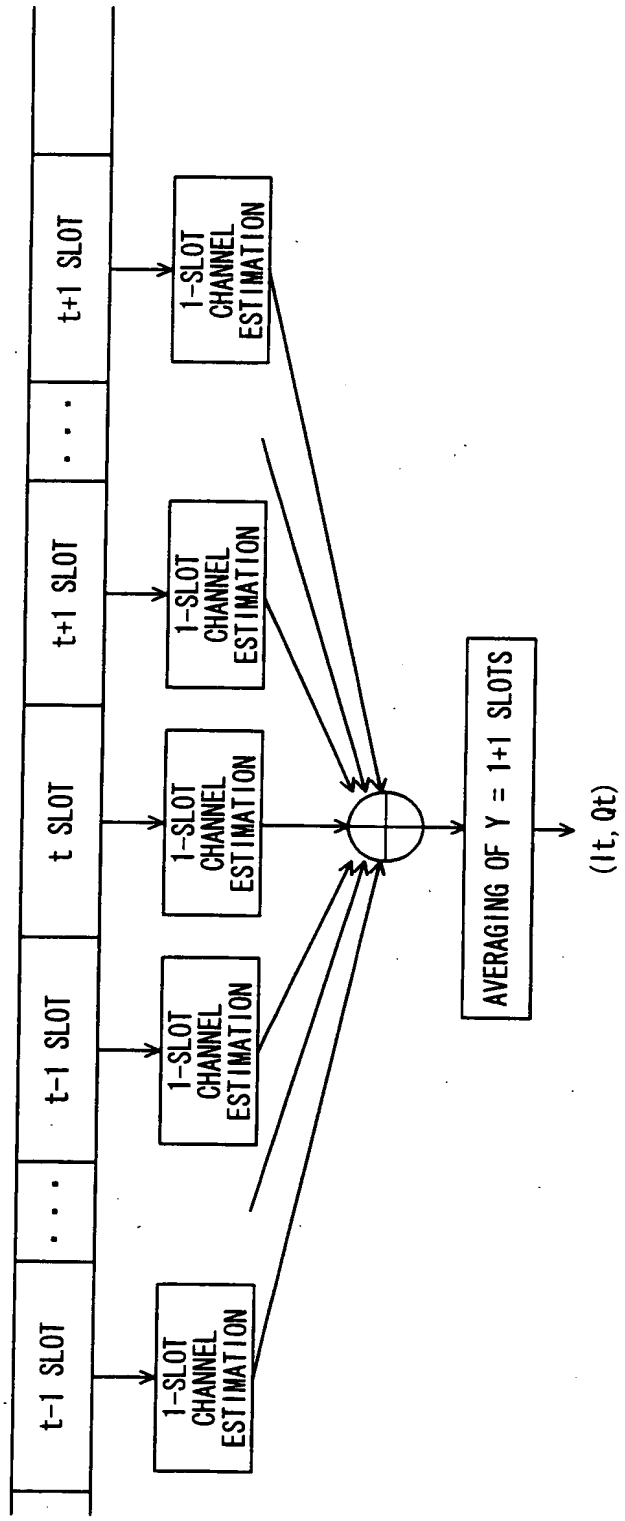


FIG. 13



APPROVED
BY
DRAFTSMAN

FIG. 14



APPROVED BY DRAFTSMAN

FIG. 15

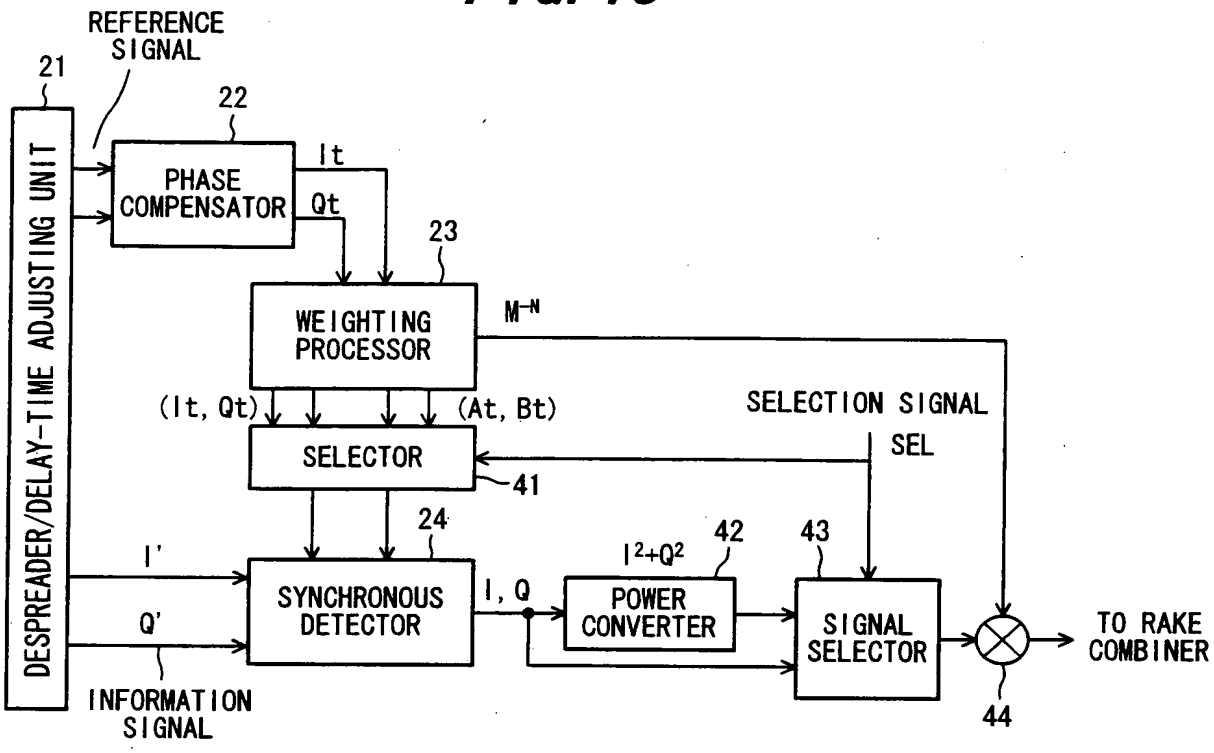
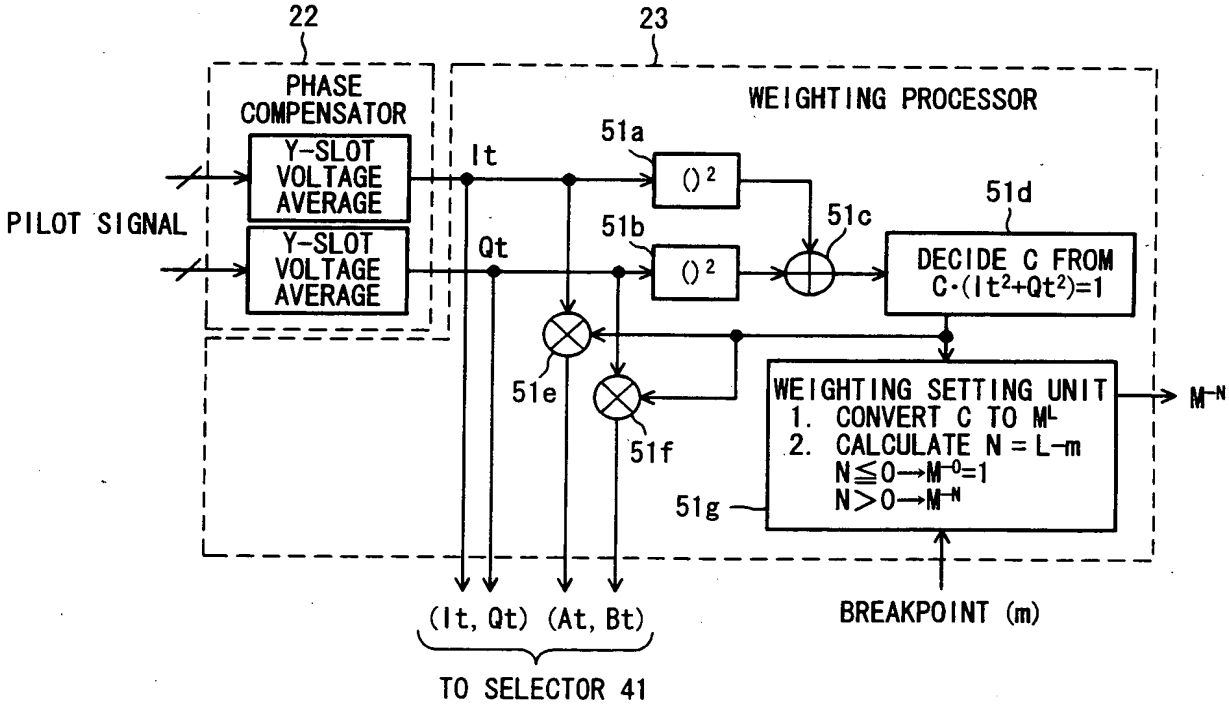


FIG. 16



APPROVED O.G.
BY
DRAFTSMAN

FIG. 17

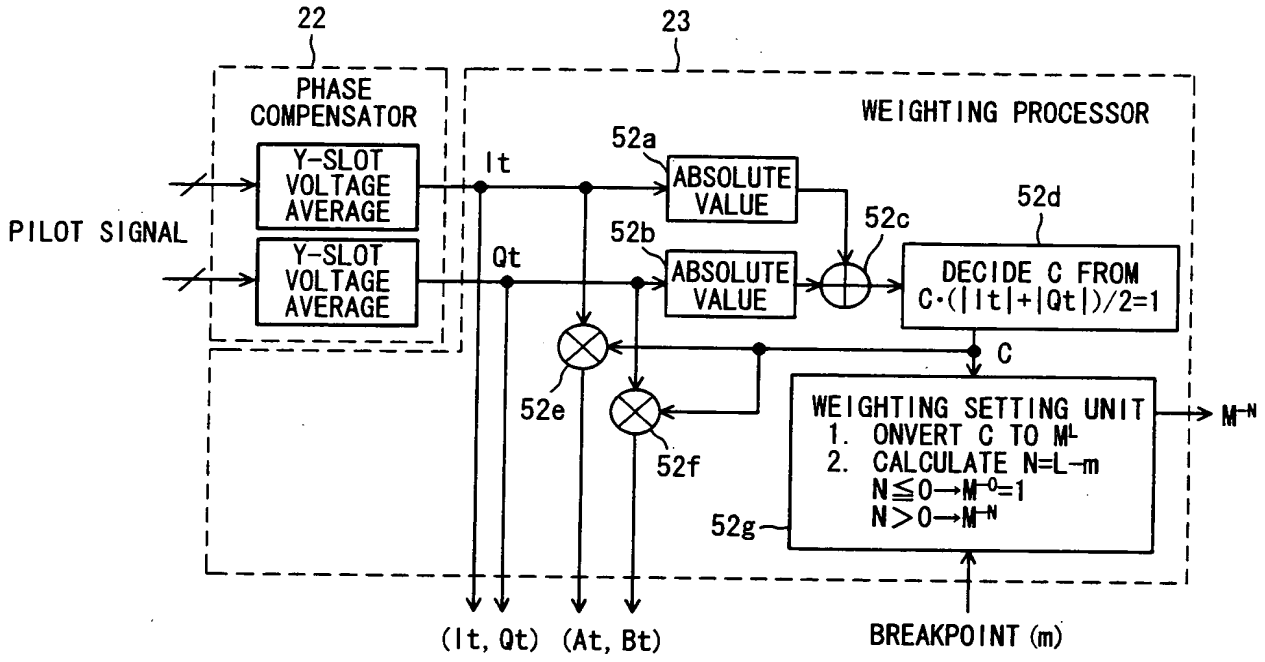
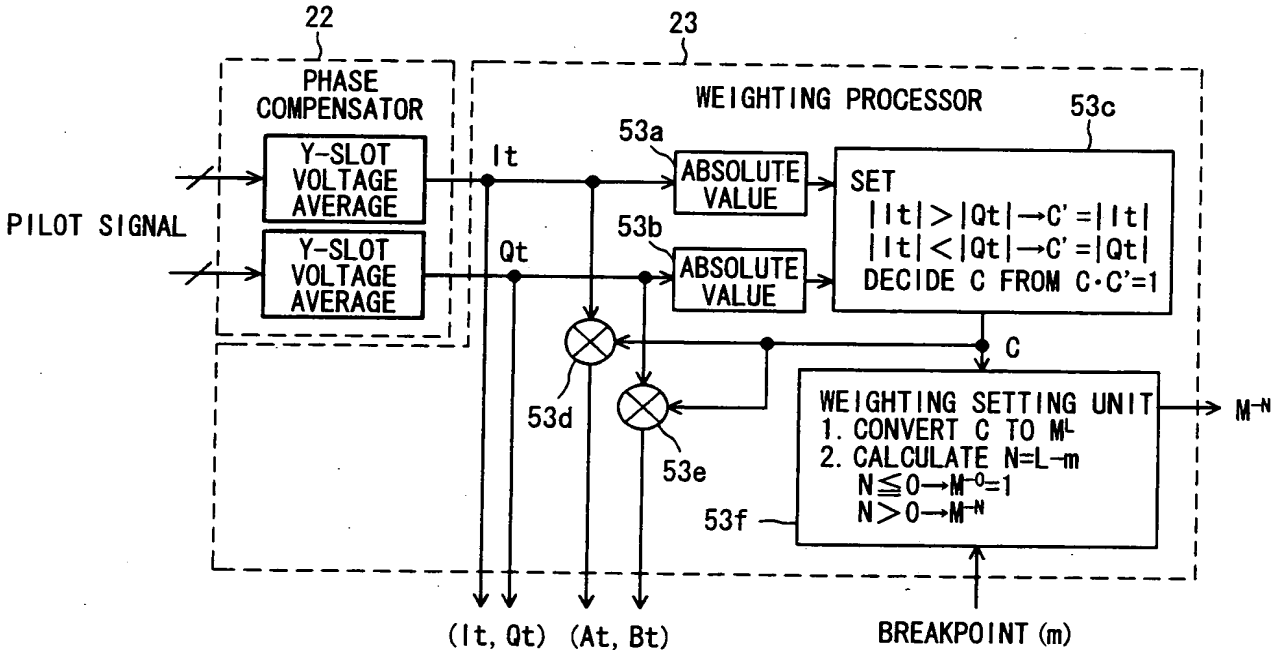


FIG. 18



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APPROVED BY DRAFTSMAN

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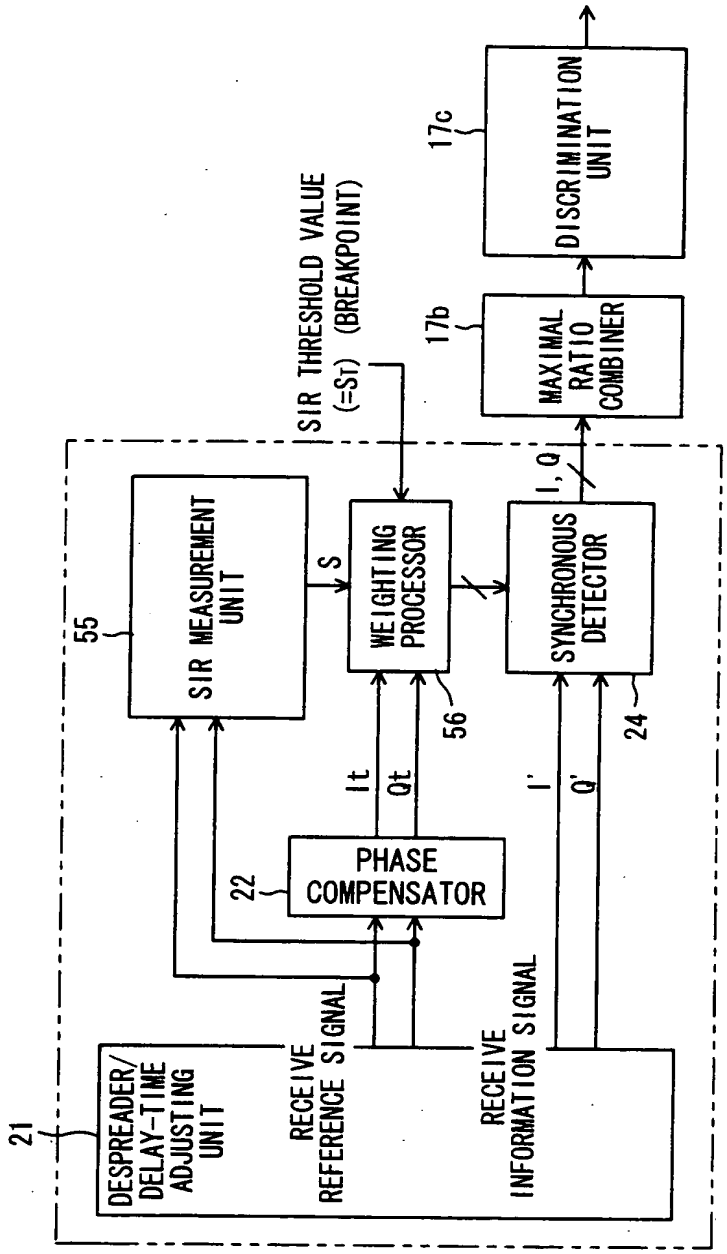
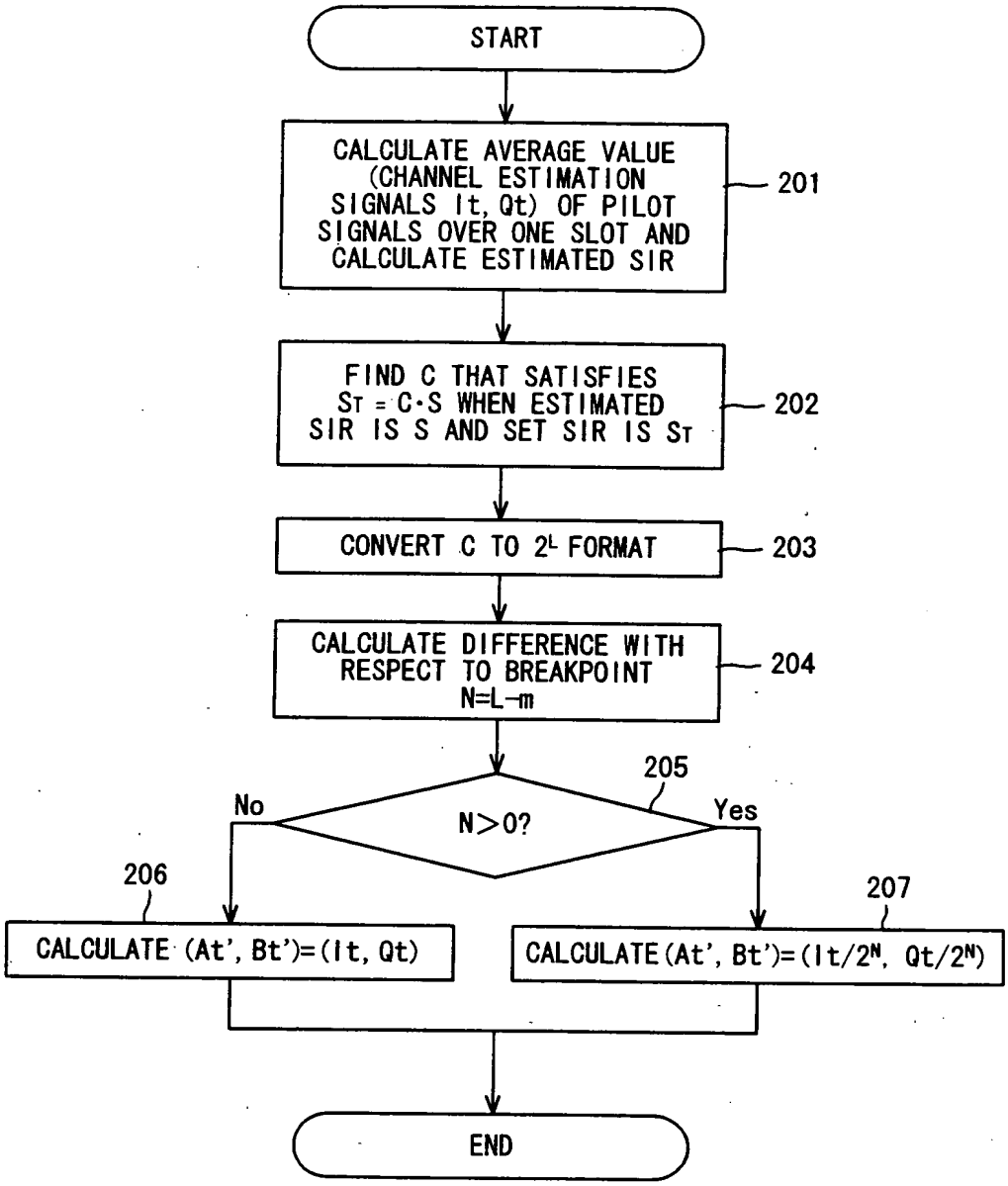
FIG. 19



APPROVED FIG.
BY CLASS
DRAFTSMAN

FIG. 20



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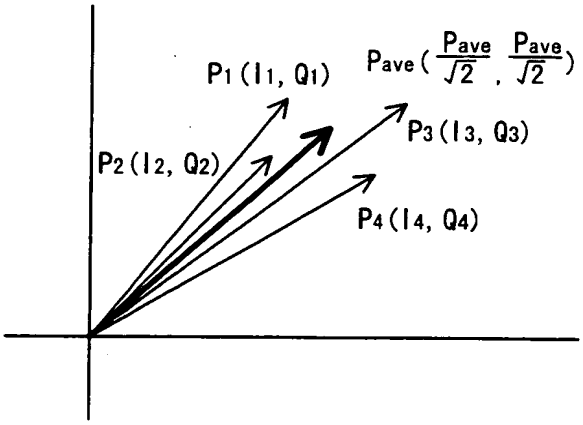
CLASS

FIG. 21



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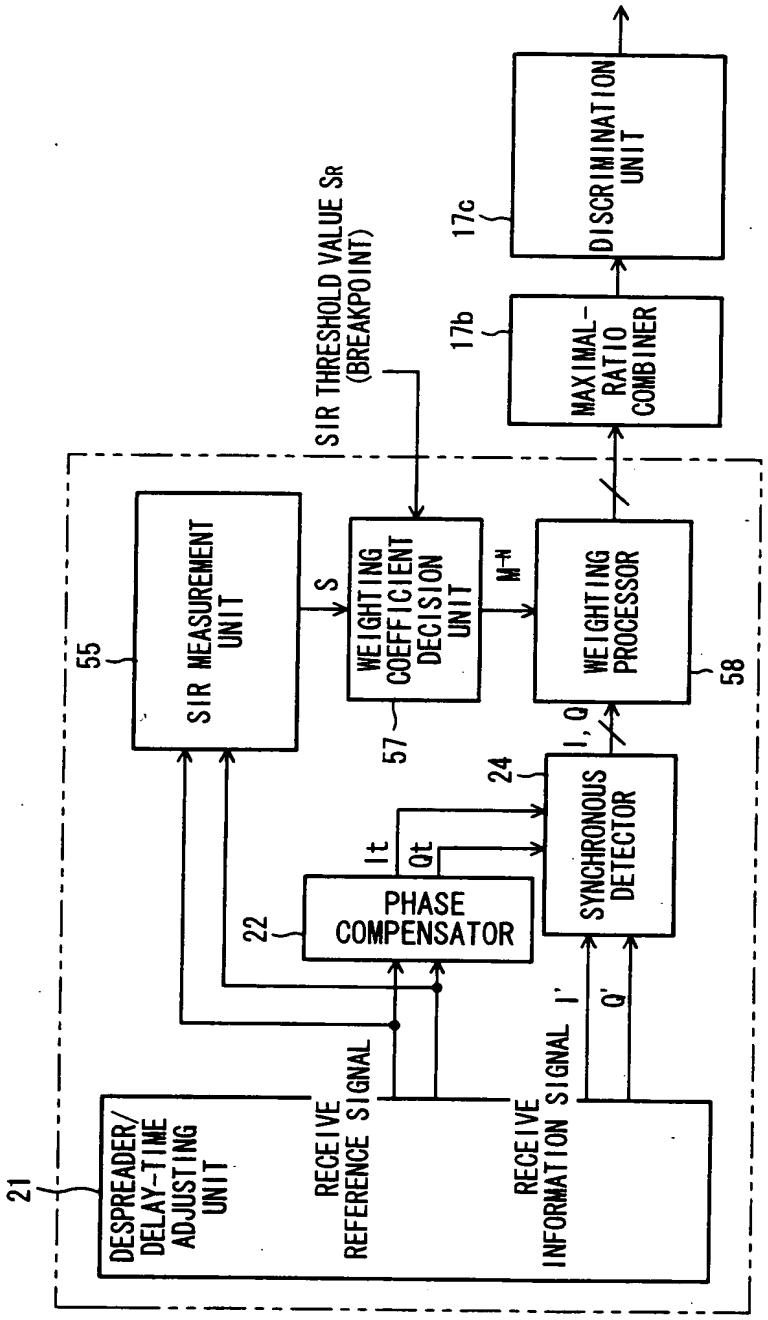
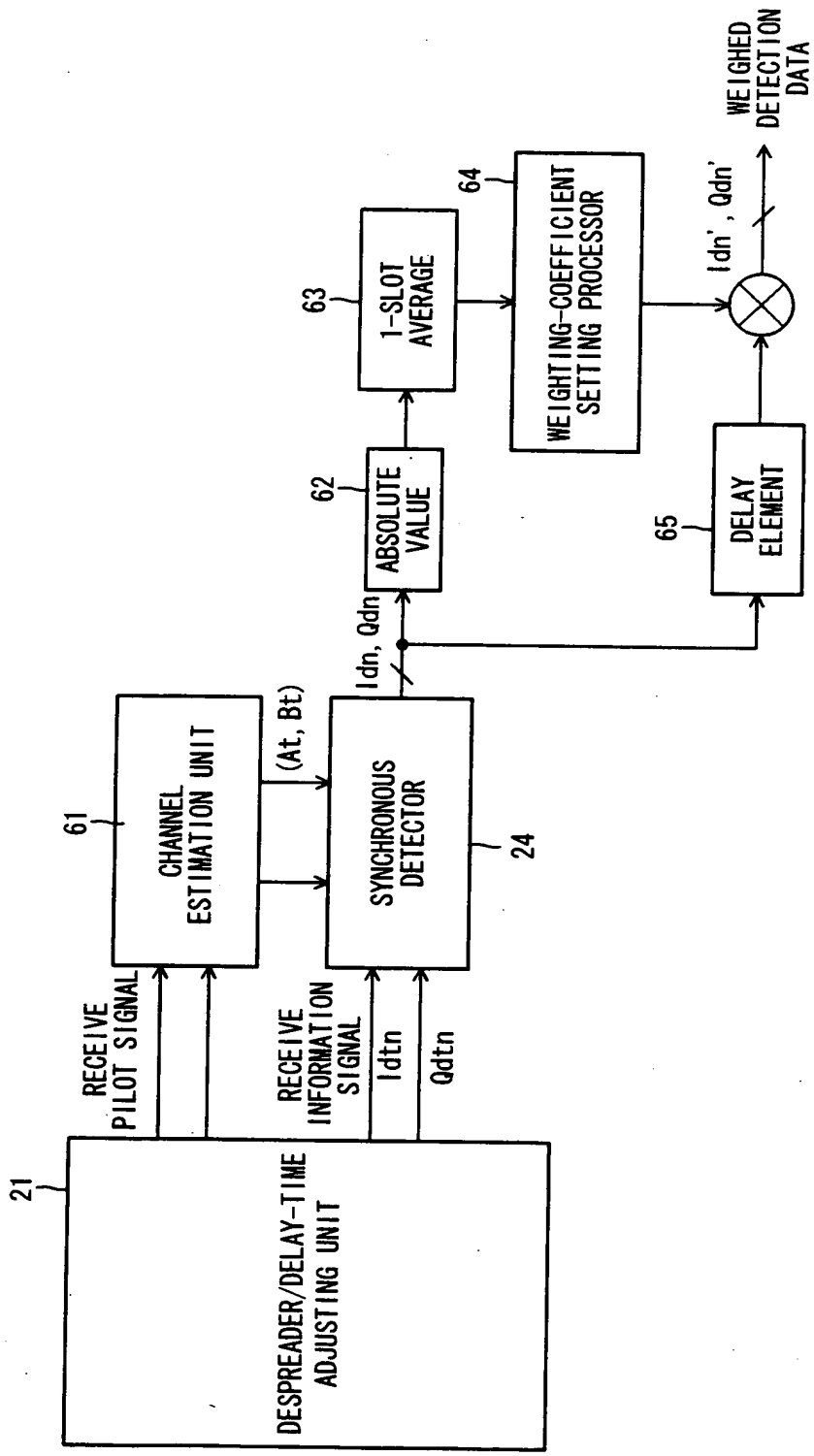
APPROVED O.I.P.E.
BY
CLASS
DRAWTSMAN

FIG. 22



APPROVED O.G. FIG.

FIG. 23



APPROVED BY DRAFTSMAN

FIG. 24

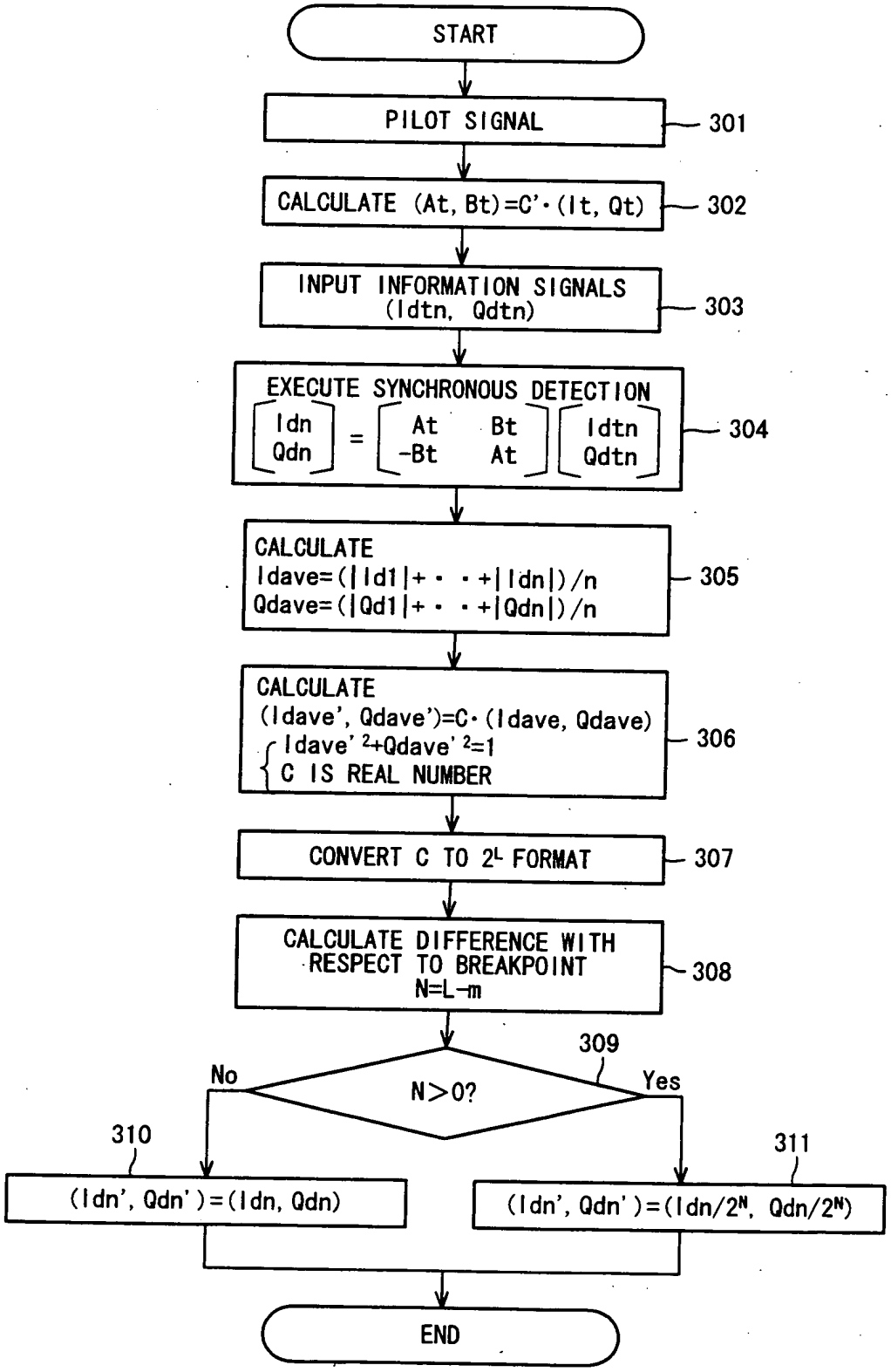


FIG. 25

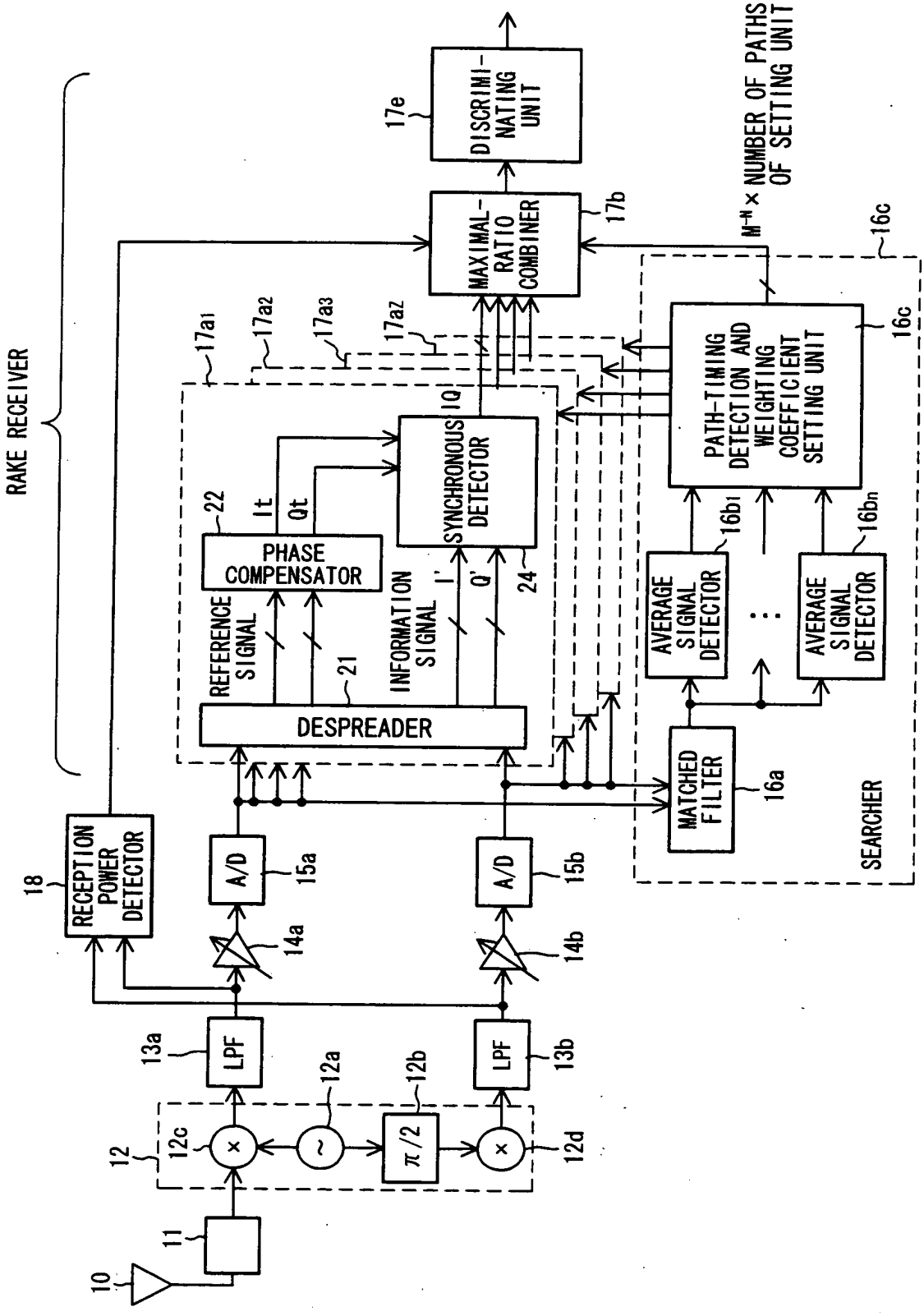
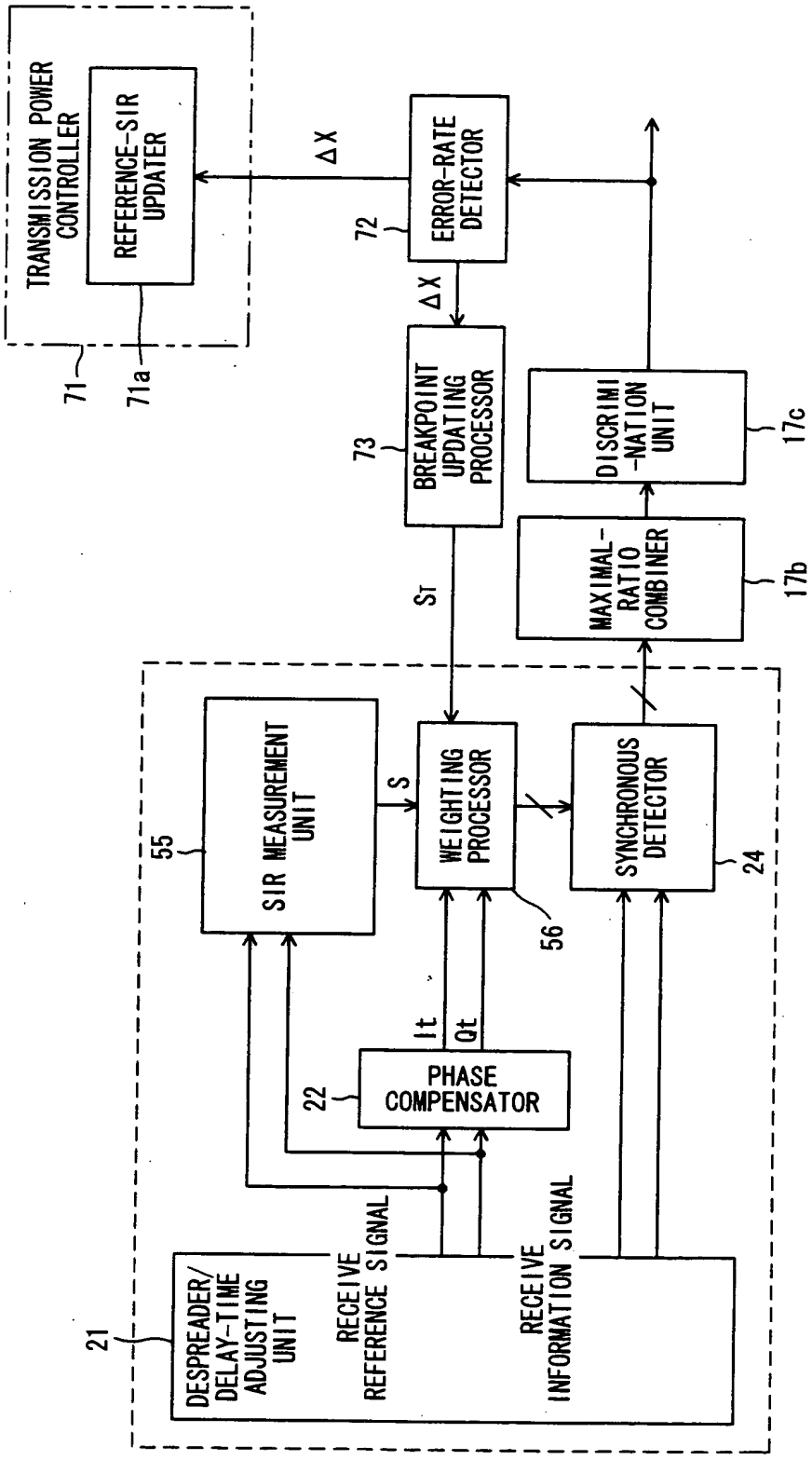

RAKE RECEIVER
SEARCHER
10
11
12
12a
12b
12c
12d
13a
13b
14a
14b
15a
15b
16a
16b1
16bn
16c
17a1
17a2
17a3
17az
17b
17e
18
21
22
24
A/D
LPF
π/2
RECEPTION POWER DETECTOR
MATCHED FILTER
AVERAGE SIGNAL DETECTOR
PATH-TIMING DETECTION AND WEIGHTING COEFFICIENT SETTING UNIT
M^H × NUMBER OF PATHS OF SETTING UNIT
MAXIMAL-RATIO COMBINER
DISCRIMINATING UNIT
DESPREADER
REFERENCE SIGNAL
INFORMATION SIGNAL
PHASE COMPENSATOR
SYNCHRONOUS DETECTOR
It
Qt
I'
Q'
IQ

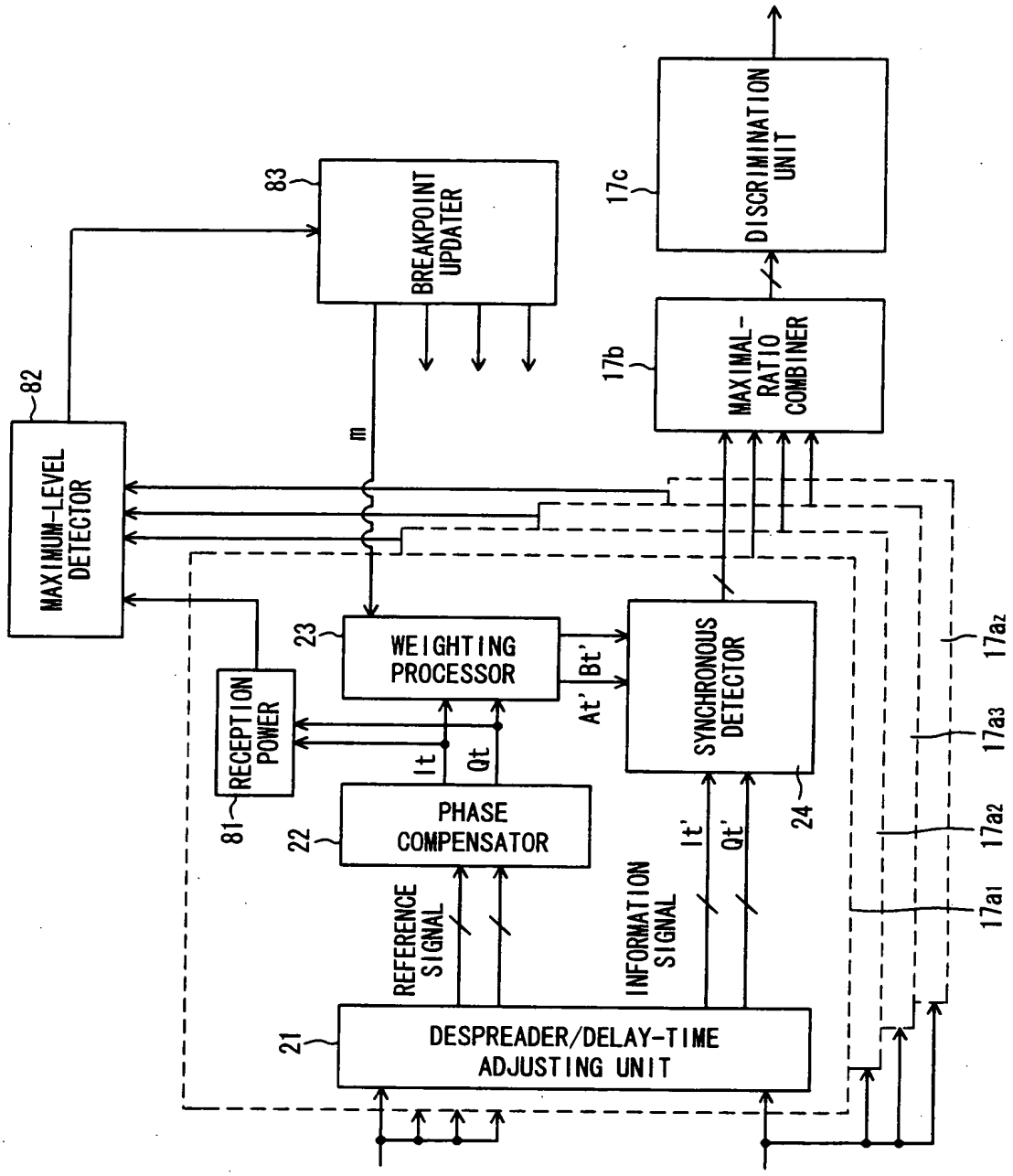
FIG. 26


TRANSMISSION POWER CONTROLLER
REFERENCE-SIR UPDATER
71
71a
ΔX
ERROR-RATE DETECTOR
72
ΔX
BREAKPOINT UPDATING PROCESSOR
73
DISCRIMI-NATION UNIT
17c
MAXIMAL-RATIO COMBINER
17b
St
SIR MEASUREMENT UNIT
55
S
WEIGHTING PROCESSOR
56
SYNCHRONOUS DETECTOR
24
It
Qt
PHASE COMPENSATOR
22
21
DESPREADER/DELAY-TIME ADJUSTING UNIT
RECEIVE REFERENCE SIGNAL
RECEIVE INFORMATION SIGNAL

APPROVED O.G. FIG.
BY
DRAFTSMAN

2004-264007

FIG. 27



APPROVED
BY
DRAFTSMAN

FIG. 28

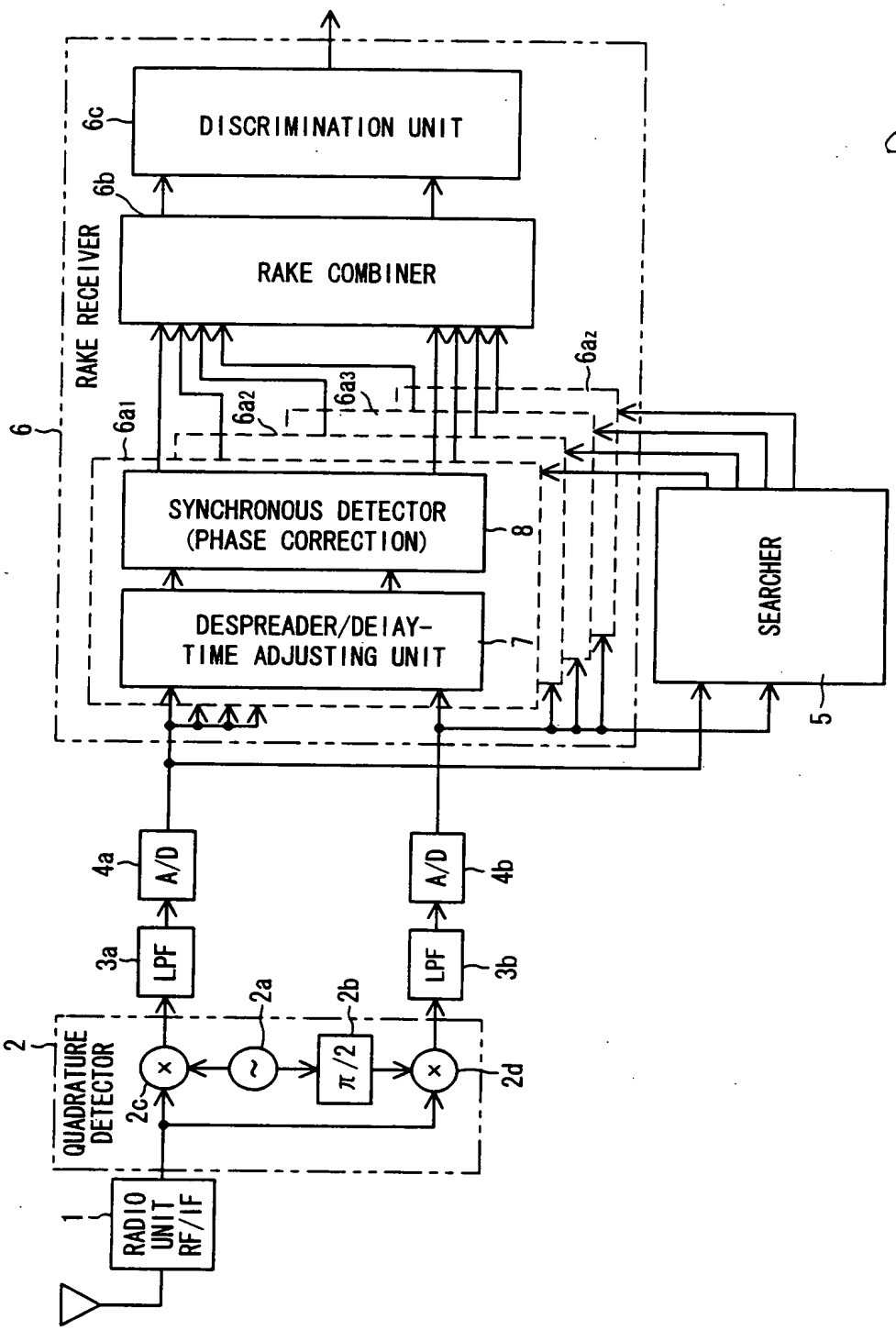


FIG. 29

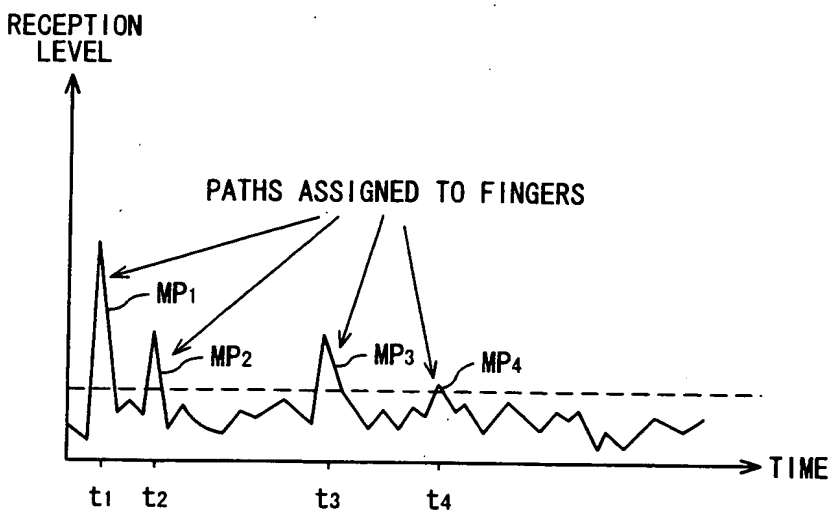

RECEPTION LEVEL
PATHS ASSIGNED TO FINGERS
MP1
MP2
MP3
MP4
TIME
t1
t2
t3
t4

FIG. 30

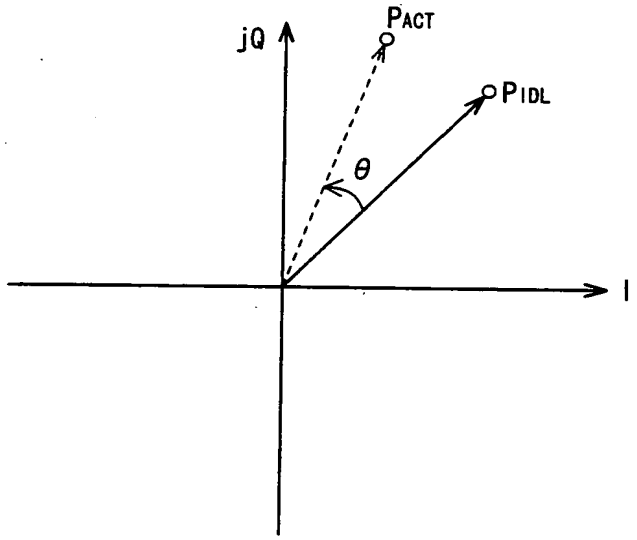

jQ
PACT
PIDL
θ
I

FIG. 31

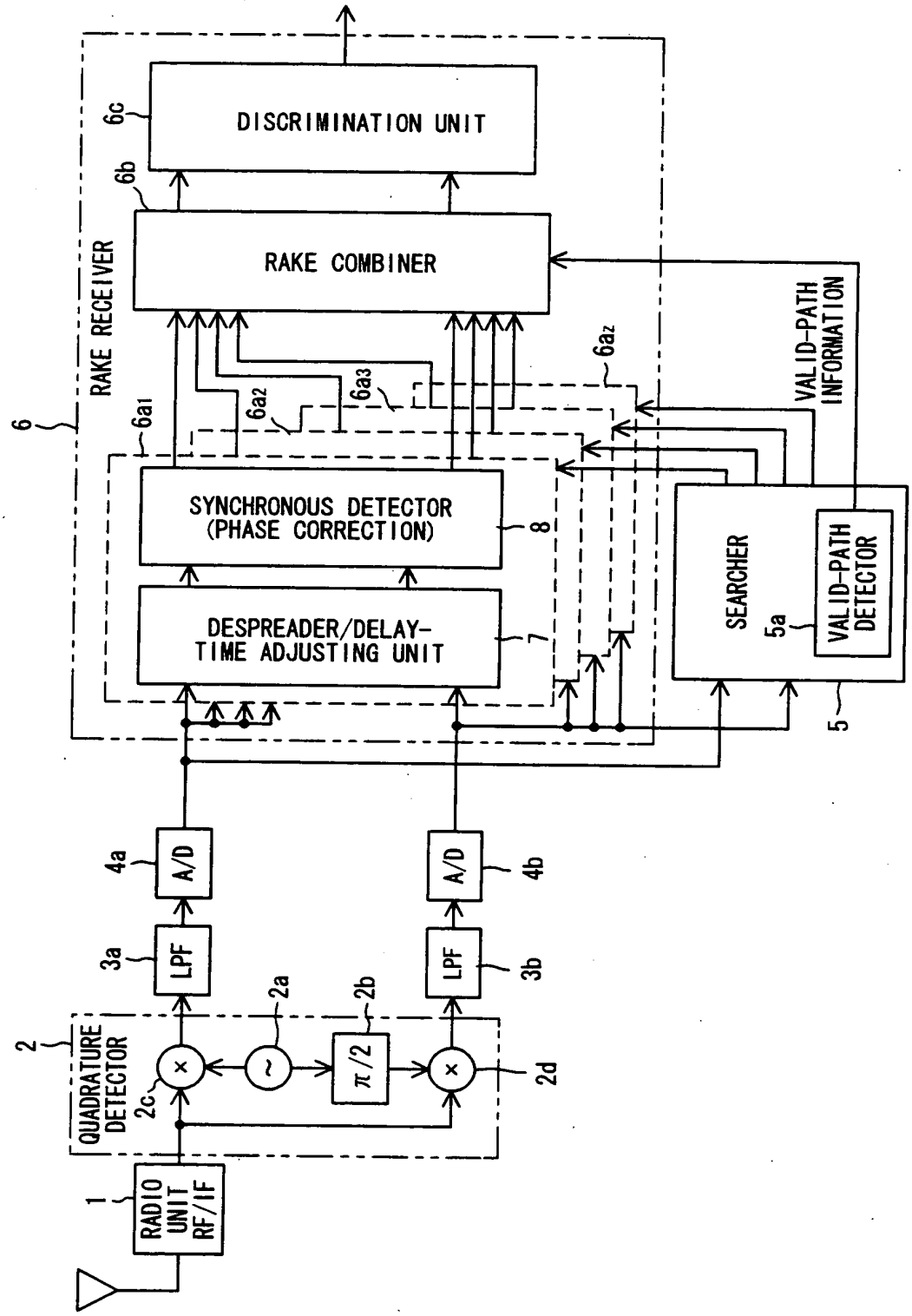

DISCRIMINATION UNIT
6c
6b
RAKE COMBINER
RAKE RECEIVER
6
6a1
6a2
6a3
6az
VALID-PATH INFORMATION
SYNCHRONOUS DETECTOR (PHASE CORRECTION)
8
DESPREADER/DELAY-TIME ADJUSTING UNIT
7
SEARCHER
5a
VALID-PATH DETECTOR
5
4a
A/D
4b
3a
LPF
3b
2a
2b
2
QUADRATURE DETECTOR
2c
π/2
2d
1
RADIO UNIT RF/IF

FIG. 32

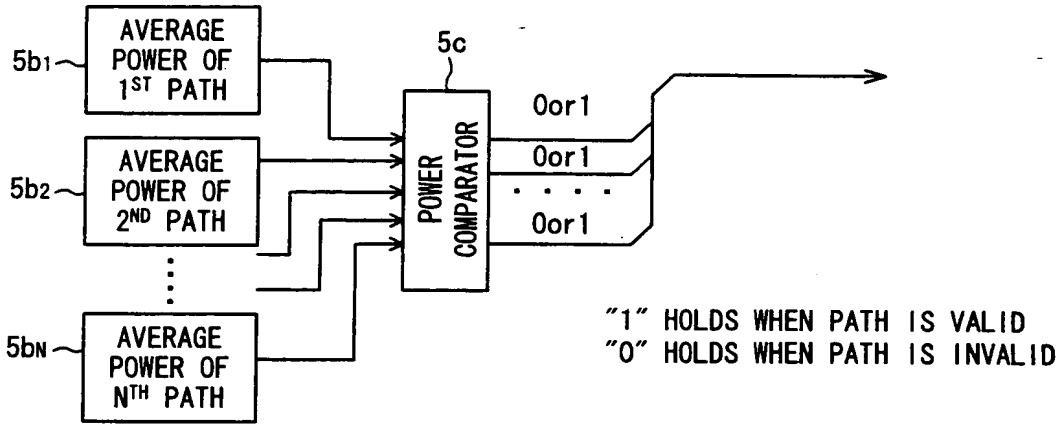
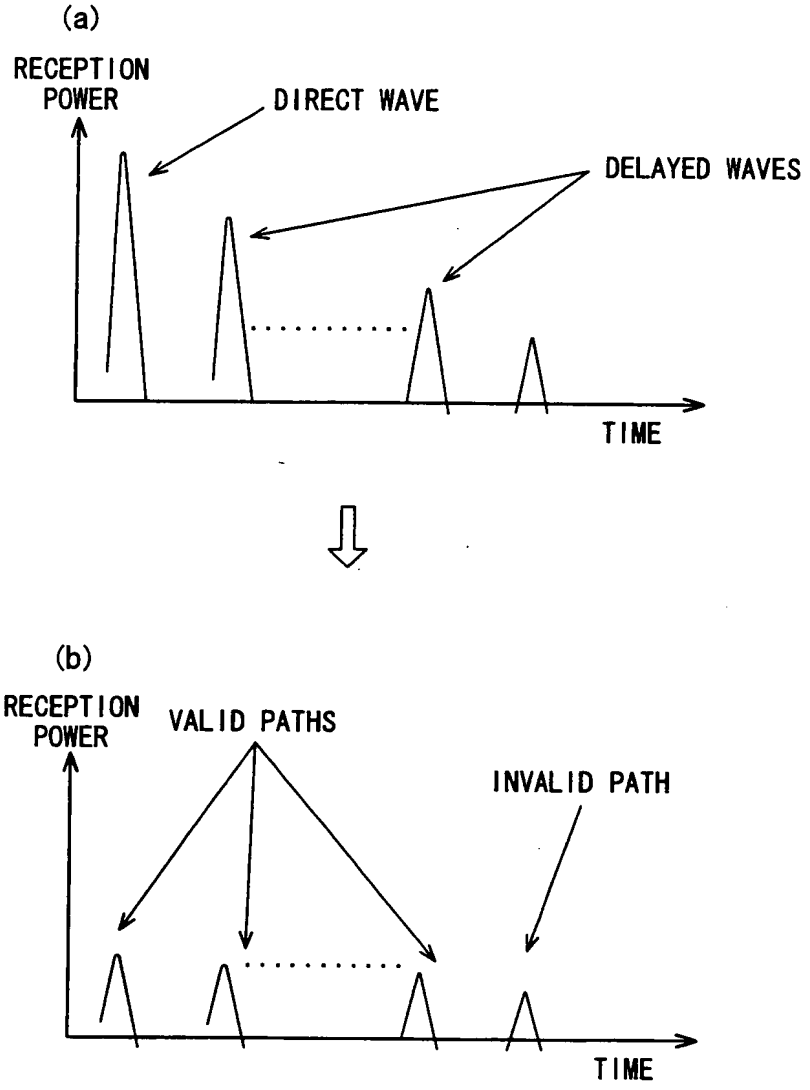

5b1
AVERAGE POWER OF 1ST PATH
5b2
AVERAGE POWER OF 2ND PATH
5bN
AVERAGE POWER OF NTH PATH
5c
POWER COMPARATOR
0or1
0or1
0or1
"1" HOLDS WHEN PATH IS VALID
"0" HOLDS WHEN PATH IS INVALID

FIG. 33


(a)
RECEPTION POWER
DIRECT WAVE
DELAYED WAVES
TIME
(b)
RECEPTION POWER
VALID PATHS
INVALID PATH
TIME